

Claims

[c1] What is claimed is:

1. A data transmission circuit for transmitting data comprising:
 - an internal circuit for providing data;
 - a register electrically connected to the internal circuit for temporarily storing the data transmitted from the internal circuit; and
 - a control circuit for controlling operations of the internal circuit and the register;wherein if data inputted to the register is a specific data, the internal circuit will repeatedly output the specific data to the register so as to prolong transmission time of the specific data.

[c2] 2. The data transmission circuit of claim 1 further comprising an output circuit electrically connected to the register for outputting the data transmitted from the register.

[c3] 3. The data transmission circuit of claim 2 wherein the register comprises a D flip-flop for outputting data transmitted from the internal circuit to the output circuit and feeding back the data to the internal circuit.

[c4] 4. The data transmission circuit of claim 2 wherein when the input circuit repeatedly outputs the specific data to the register, the register will prolong the time that the specific data is outputted to the output circuit, and when the register prolongs the time that the specific data outputted to the output circuit, the control circuit will output a control signal to terminate the register from outputting the specific data to the output circuit.

[c5] 5. The data transmission circuit of claim 1 wherein the input circuit comprises a multiplexer, the multiplexer comprising:

- a first input end electrically connected to an output end of the register for inputting data outputted by the register;
- a second input end for inputting data to be transmitted by the data transmission circuit; and
- a selecting end electrically connected to the control circuit for receiving a selecting signal transmitted from the control circuit.

[c6] 6.The data transmission circuit of claim 5 wherein when the second input end receives the specific data and the multiplexer has transmitted the specific data to the register, the control circuit will use the selecting signal to the control end so that the multiplexer will continue to output the specific data to the register.

[c7] 7.The data transmission circuit of claim 5 wherein the specific data is a last data packet within a plurality of data packets successively inputted to the second input end.

[c8] 8.The data transmission circuit of claim 1 being formed on a north bridge chip of a motherboard.

[c9] 9.A data transmission method of a data transmission circuit, the data transmission circuit comprising:
an internal circuit for providing data; and
a register electrically connected to the input circuit for temporarily storing data transmitted from the input circuit;
the method comprising:
data inputted to the input circuit being specific data, the specific data will be repeatedly transmitted to the register to prolong transmission time of the specific data.

[c10] 10.The data transmission method of claim 9 further comprising:
for prolonged transmission time of the specific data , an control signal will be transmitted to terminate transmission of the specific data.

[c11] 11.The data transmission method of claim 9 wherein the specific data being a last data packet of a plurality of data packets gets successively inputted to the input circuit.

[c12] 12.The data transmission method of claim 9 wherein the data transmission circuit being located on a north bridge of a motherboard.